## IN THE CLAIMS:

Please amend the claims as follows:

- 1. (Currently Amended) A method of filling one or more of a via and a trench in a patterned substrate, comprising:
- a) depositing a generally conformal first barrier layer <u>in one or more of the via</u> <u>and the trench</u> on the patterned substrate by chemical vapor deposition, <u>wherein the first barrier layer is selected from the group consisting of TiSi<sub>x</sub>N, TiN(C), TiNSi(C), Ta, TaN<sub>1</sub>(C), TaNSi(C), W, WN<sub>x</sub>, SiO<sub>x</sub>N<sub>y</sub>, SiC, AlN, and Al<sub>2</sub>O<sub>3</sub>;</u>
- b) removing the first barrier layer from the horizontal surfaces of the patterned substrate;
  - c) depositing a second barrier layer by physical vapor deposition; and then
  - d) depositing one or more conductive materials.
- 2. (Original) The method of claim 1 wherein depositing the conductive material comprises depositing a seed layer and a metal layer in the via and/or the trench after the second barrier layer is deposited.
- 3. (Currently Amended) The method of claim 2 wherein the first barrier layer is selected from the group consisting of  $Si_xN_y$ ,  $TiSi_xN$ , TiN(C), TiNSi(C), TaNSi(C), TiNSi(C), TiNSi(C),
- 4. (Currently Amended) The method of claim  $3 \underline{1}$  wherein the second barrier layer is selected from the group consisting of Ta, TaN, TiSiN<sub>x</sub>, TaSiN<sub>x</sub>, W, and WN<sub>x</sub>.
- 5. (Original) The method of claim 4 wherein the seed layer is copper.
- 6. (Original) The method of claim 5 wherein the metal layer is copper.

- 7. (Original) The method of claim 1 wherein the first barrier layer is deposited and removed from the horizontal surfaces of the patterned substrate within a single chamber of an integrated processing tool.
- 8. (Original) The method of claim 7 wherein the chamber is a chemical vapor deposition chamber and the first barrier layer is deposited and etched in the chamber.
- 9. (Original) The method of claim 2 wherein the seed layer is deposited by physical vapor deposition.
- 10. (Original) The method of claim 2 wherein the seed layer is deposited by chemical vapor deposition.
- 11. (Original) The method of claim 2 wherein the seed layer is deposited by electroless deposition.
- 12. (Original) The method of claim 2 wherein the metal layer is deposited by physical vapor deposition.
- 13. (Original) The method of claim 2 wherein the metal layer is deposited by chemical vapor deposition.
- 14. (Original) The method of claim 2 wherein the metal layer is deposited by electroplating.
- 15. (Original) The method of claim 1 wherein the via has an aspect ratio of about 4 to 1 and the trench has an aspect ratio of about 1 to 1.
- 16. (Original) The method of claim 1 wherein the second barrier layer has a thickness of from about 20 Å to about 50 Å at the bottom of the via.

- 17. (Original) The method of claim 1 wherein the second barrier layer is selected from the group consisting of Ta, TaN, W, WN<sub>x</sub>, Ti, and TiN, and the second barrier layer has a thickness of from about 20 Å to about 50 Å at the bottom of the via.
- 18. (Original) A method of filling one or more of a via and a trench in a patterned substrate, comprising:
- a) depositing a generally conformal first barrier layer on the patterned substrate by atomic layer deposition;
- b) removing the first barrier layer from the horizontal surfaces of the patterned substrate;
  - c) depositing a second barrier layer by physical vapor deposition; and then
  - d) depositing one or more conductive materials.
- 19. (Original) The method of claim 18 wherein depositing the conductive material comprises depositing a seed layer and a metal layer in the via and/or the trench after the second barrier layer is deposited.
- 20. (Original) The method of claim 19 wherein the first barrier layer is selected from the group consisting of Ta, TaN, W, and WN.
- 21. (Original) The method of claim 20 wherein the second barrier layer is selected from the group consisting of Ta, TaN, TiSiN<sub>x</sub>, TaSiN<sub>x</sub>, W, and WN<sub>x</sub>.
- 22. (Original) The method of claim 21 wherein the seed layer is copper.
- 23. (Original) The method of claim 22 wherein the metal layer is copper.
- 24. (Original) The method of claim 18 wherein the first barrier layer is deposited and removed from the horizontal surfaces of the patterned substrate within a single chamber of an integrated processing tool.

- 25. (Original) The method of claim 24 wherein the chamber is an atomic layer deposition chamber and the first barrier layer is deposited and etched in the chamber.
- 26. (Original) The method of claim 19 wherein the seed layer is deposited by physical vapor deposition.
- 27. (Original) The method of claim 19 wherein the seed layer is deposited by chemical vapor deposition.
- 28. (Original) The method of claim 19 wherein the seed layer is deposited by electroless deposition.
- 29. (Original) The method of claim 19 wherein the metal layer is deposited by physical vapor deposition.
- 30. (Original) The method of claim 19 wherein the metal layer is deposited by chemical vapor deposition.
- 31. (Original) The method of claim 19 wherein the metal layer is deposited by electroplating.
- 32. (Original) The method of claim 18 wherein the via has an aspect ratio of about 4 to 1 and the trench has an aspect ratio of from about 1 to about 1.
- 33. (Original) The method of claim 18 wherein the second barrier layer has a thickness of from about 20 Å to about 50 Å at the bottom of the via.
- 34. (Original) The method of claim 18 wherein the second barrier layer is selected from the group consisting of Ta, TaN, W, WN<sub>x</sub>, Ti, and TiN, and the second barrier layer has a thickness of from about 20 Å to about 50 Å at the bottom of the via.

- 35. (Original) A method of filling one or more of a via and a trench in a patterned substrate having an etch stop at the via level, comprising:
- a) depositing a generally conformal first barrier layer on the patterned substrate by chemical vapor deposition;
- b) removing the first barrier layer from the horizontal surfaces of the patterned substrate;
  - c) removing the etch stop from the bottom of the via;
  - d) depositing a second barrier layer by physical vapor deposition; and then
  - e) depositing one or more conductive materials.
- 36. (Original) The method of claim 35 wherein depositing the conductive material comprises depositing a seed layer and a metal layer in the via and/or the trench after the second barrier layer is deposited.
- 37. (Currently Amended) A method of filling one or more of a via and a trench in a patterned substrate having a metal layer underlying the via, comprising:
- a) depositing a generally conformal first barrier layer on the patterned substrate by chemical vapor deposition, wherein the first barrier layer is selected from the group consisting of TiSi<sub>x</sub>N, TiN(C), TiNSi(C), Ta, TaC, TaN(C), TaNSi(C), W, WN<sub>x</sub>, SiO<sub>x</sub>N<sub>y</sub>, SiC, AlN, and Al<sub>2</sub>O<sub>3</sub>;
- b) removing the first barrier layer from the horizontal surfaces of the patterned substrate;
- c) depositing by physical vapor deposition a second barrier layer sufficient to provide a barrier on the bottom of the trench without significantly impairing conduction between the conductive material deposited in the via and the metal layer; and then
  - d) depositing one or more conductive materials.
- 38. (Original) A method of filling one or more of a via and a trench in a patterned substrate having a metal layer underlying the via, comprising:
- a) depositing a generally conformal first barrier layer on the patterned substrate by atomic layer deposition;

- b) removing the first barrier layer from the horizontal surfaces of the patterned substrate;
- c) depositing by physical vapor deposition a second barrier layer sufficient to provide a barrier on the bottom of the trench without significantly impairing conduction between the conductive material deposited in the via and the metal layer; and then
  - d) depositing one or more conductive materials.

39-40. (Canceled)